

		Class	Subclass
ISSUE CLASSIFICATION			

HKM O.I.P.E. 3 SCANNED <i>PM</i> 22 <i>CH</i>	PATENT DATE
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APPLICATION NO. 09/739219	CONT/PRIOR F	CLASS 716.380	SUBCLASS 29	ART UNIT 2131 2825	EXAMINER HAYES Chen
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Takeshi Shimoyama

Method and apparatus for designing cipher logic, and a computer product

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Date)			
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	_____ (Date)		<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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